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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
**UTILITY PATENT
APPLICATION TRANSMITTAL LETTER**
Box PATENT APPLICATION

 Assistant Commissioner for Patents
 Washington, D.C. 20231

Sir:

Enclosed for filing is the utility patent application of Akio KATSUBE, Hideo NAKAGOSHI and Hidemasa KAWAI for HOLDING JIG FOR ELECTRONIC PARTS, HOLDING METHOD THEREFOR, AND MANUFACTURING METHOD FOR ELECTRONIC PARTS.

Also enclosed are:

- 6 sheet(s) of [] formal informal drawing(s);
- a claim for foreign priority under 35 U.S.C. §§ 119 and/or 365 is hereby made to 11-291575 and 11-291576 filed in Japan on October 13, 1999;
- in the declaration;
- certified copies of the priority documents;
- a General Authorization for Petitions for Extensions of Time and Payment of Fees;
- an Assignment document;
- an Information Disclosure Statement; and
- Other: _____

- An [] executed unexecuted declaration of the inventor(s)
- also is enclosed [] will follow.
- Please amend the specification by inserting before the first line the sentence --This application claims priority under 35 U.S.C. §§ 119 and/or 365 to filed in on ; the entire content of which is hereby incorporated by reference.--
- A bibliographic data entry sheet is enclosed.
- Small entity status is hereby claimed.

- The filing fee has been calculated as follows [] and in accordance with the enclosed preliminary amendment:

**21839**

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Utility Patent Application Transmittal Letter

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C L A I M S					
	NO. OF CLAIMS		EXTRA CLAIMS	RATE	FEE
Basic Application Fee					\$710.00 (101)
Total Claims	14	MINUS 20 =	0	× \$18.00 (103) =	0.00
Independent Claims	4	MINUS 3 =	1	× \$80.00 (102) =	80.00
If multiple dependent claims are presented, add \$270.00 (104)					
Total Application Fee					790.00
If small entity status is claimed, subtract 50% of Total Application Fee					
Add Assignment Recording Fee \$ if Assignment document is enclosed					
TOTAL APPLICATION FEE DUE					\$790.00

This application is being filed without a filing fee. Issuance of a Notice to File Missing Parts of Application is respectfully requested.

A check in the amount of \$ 790.00 is enclosed for the fee due.

Charge \$ _____ to Deposit Account No. 02-4800 for the fee due.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Please address all correspondence concerning the present application to:

Platon N. Mandros
 BURNS, DOANE, SWECKER & MATHIS, L.L.P.
 P.O. Box 1404
 Alexandria, Virginia 22313-1404.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

Date: October 13, 2000

By: Timothy B. Kang *Timothy B. Kang*
 for Adam J. Cermak
 Registration No. 40,391

P.O. Box 1404
 Alexandria, Virginia 22313-1404
 (703) 836-6620

HOLDING JIG FOR ELECTRONIC PARTS, HOLDING
METHOD THEREFOR, AND MANUFACTURING
METHOD FOR ELECTRONIC PARTS

This application is related to and claims priority under 35 U.S.C. §§119
5 and/or 365 to Japanese Patent Application No. 11-291575, filed in Japan on
October 13, 1999; the entire contents of which is hereby incorporated by
reference.

FIELD OF THE INVENTION

This invention relates to a holding jig for holding electronic parts such as
10 semiconductor integrated circuits, and the like, when the electronic parts are being
manufactured, to a holding method therefor, and to a manufacturing method for
the electronic parts.

BACKGROUND OF THE INVENTION

Generally, in handling electronic parts or components constituting
15 electronic parts in manufacturing processes for electronic parts, there are ways to
handle a plurality of parts together and ways to handle each part separately. When
electronic parts are manufactured in the latter way, up to now, a holding jig for
holding electronic parts and components thereof, for example, as shown in Fig. 7,
has been used in order to handle each of the parts collectively. In Fig. 7, a metal
20 tray 11 is shown and cavities 12 for arranging a plurality of parts are formed by
press molding, or the like, in advance. When semiconductor chips, for example,
are wire bonded by using such a holding jig, substrates are arranged in the tray 11,
the semiconductor chips are die bonded on each of the substrates, and then the
chips are wire bonded. However, in these processes, because the substrates are
25 required to be fixed in the tray 11, the upper surface of the tray 11 was covered by
a pressure jig 13, where openings 14 in accordance with the arrangement of the

substrates, and a pressure leaf spring 15 for fixing each of the substrates, are formed. When the substrates are disposed inside the concave cavities, clearance is required between the cavity and the substrate in consideration of the ease of inserting and removing substrates and dimensional tolerance limits. Therefore, the 5 position of the substrate inside the cavity may vary, and moreover because the substrate is displaced when it is transferred between processes, the coordinate position of the substrate becomes different inside the cavity, depending on the processes. As a result, when each process is automated, errors in positional 10 recognition are likely to occur, and an additional process for correcting the errors is required.

Furthermore, when the substrate is held down by the pressure leaf spring 15 shown in Fig. 7, an extra space for the pressure leaf spring 15 is required at the substrate. Because of this, it is difficult to make the electronic parts small. In particular, if the pressure leaf spring is made too small, it is difficult to securely 15 hold the substrate, and accordingly the smaller the size of the substrate, the higher the ratio of the space occupied by the pressure leaf spring to the size of the substrate, and therefore miniaturization of the substrate is obstructed.

Furthermore, because the above-mentioned cavity is determined by the size of the substrate and the arrangement pattern of a plurality of substrates, an 20 exclusive tray is required for electronic parts of one kind and is not appropriate for broader use. Therefore, the costs, such as the processing cost, material cost, die cost, and the like, for making the tray is substantially increased.

Accordingly, in Japanese Examined Patent Application Publication No. 7-93247, the applicant of the application concerned, applied for a holding jig for 25 small parts and for a holding method therefor which can solve the various above-mentioned problems.

In the above holding jig, an elastic rubber material, at least the surface of which is adhesive, is used, and small parts are made to be held on the surface to which the parts adhere.

According to such a holding jig and holding method, advantages such as 5 the capability of fixing parts in place, the possibility for broad use and miniaturization, for example, are possible.

However, general characteristics of rubber materials are that the volume resistivity is extremely high and insulation characteristics are exhibited.

Accordingly, electrostatic charges are likely to be caused in the insertion and 10 withdrawal, transport, and manufacturing processes of electronic parts or their components. Therefore, the following problems may tend to occur.

- 1) If such a holding jig and holding method are applied to electronic parts having low electrostatic withstand voltages, there is a risk that the electronic parts will be subjected to electrostatic discharge damage during manufacturing.
- 15 2) When small, thin, and light electronic parts or components thereof are handled, holding failure is likely to occur because of electrostatic attraction or repulsion due to the above-mentioned electrostatic charges, and there is a risk that the electronic parts will be damaged and lost and that the holding jig will be damaged.
- 20 3) Even when such a holding jig and holding method are applied to electronic parts other than those in 1) and 2), there is a possibility of damaging the electronic parts if electrostatic charges are not eliminated prior to use or during use of the holding jig. Therefore, the necessity of providing exclusive equipment for eliminating the electrostatic charges arises.

25

SUMMARY OF THE INVENTION

It is an object of the present invention to provide a holding jig for electronic parts, a holding method therefor, and a manufacturing method for the

electronic parts, in which, by solving the above-mentioned problems, increases in productivity and reliability of the electronic parts and reduction in manufacturing costs are made possible.

In the present invention, an elastic material, at least a surface of which is adhesive and electrically conductive, is provided. Due to the adhesive strength at the surface of the elastic material, electronic parts or components constituting the electronic parts are held on the surface of the elastic material. Thus, generation of electrostatic charges by the elastic material is prevented, and as electrostatic charges from elsewhere are quickly discharged, the application of high voltage to electronic parts or components thereof or the flow of electric current at electric discharge does not occur.

Furthermore, in the present invention, by adding conductive material to the elastic material, the surface of the elastic material is made conductive. Thus, the entire elastic material is given a predetermined conductivity, and accordingly, electrical conductivity to the elastic material is ensured even for extremely small chip-type parts.

Furthermore, in the present invention, by installing a wiring using electrically conductive material on the surface of the elastic material, the surface of the elastic material is made conductive.

Furthermore, in the present invention, by installing wiring using conductive material inside the elastic material, which is exposed on the surface of the elastic material, the surface of the elastic material is made conductive. Thus, the electrical conductivity of the elastic material is further increased and the shortening of the current path is targeted.

Furthermore, in the present invention, while a substrate is held on the surface of a holding jig having an elastic material, at least the surface of which exhibits adhesive properties and electrical conductivity, due to the adhesive strength of the surface, an element may be mounted on and electrically connected

to the substrate. Thus, the element is mounted on the substrate without subjecting the element to electrostatic discharge damage.

In the present invention, a holding jig having an elastic material, at least the surface of which exhibits adhesive properties, is used, and while a substrate is held on the surface of the elastic material by the adhesive strength of the surface, an element is mounted on and electrically connected to the substrate.

Furthermore, in the present invention, in the mounting process, ultrasonic waves are applied to the connecting portion where electrical connection is performed. Thus, even small electronic parts maintain a fixed bonding strength.

Furthermore, in the present invention, the hardness of the elastic material is a rubber hardness degree of A30 or more. Thus, the displacement of a substrate being held by adhesion is suppressed, and furthermore, the absorption of ultrasonic wave energy is suppressed.

Furthermore, in the present invention, the holding jig contains heat-resistant material having a heat-resistance temperature of 250°C. Thus, an element can be mounted on a substrate by wire bonding or bump bonding by heating while using the holding jig. Accordingly, the time required for bonding is reduced and the bonding strength is increased.

Furthermore, in the present invention, the holding jig is a laminated structure made of a hard plate and an elastic material. Thus, the flatness of the surface of the elastic material is increased.

Furthermore, in the present invention, the primary material of the elastic material is silicone resin. Thus, the deterioration of the elastic material with the passage of time is prevented and stability is increased.

Furthermore, in the present invention, in a wire bonding process, an element is electrically connected to the surface. Thus, the element, on the upper surface of which a pad is contained, is electrically connected to an electrode on the substrate.

Furthermore, in the present invention, in a bump bonding process, an element is electrically connected to the substrate. Thus, an element, on the lower surface of which an electrode for connection is contained, is electrically connected to an electrode on the substrate through the bump.

5

BRIEF DESCRIPTION OF THE DRAWINGS

Figs. 1A and 1B are a perspective view and a sectional view showing the structure of a holding jig to be used in a manufacturing method for electronic parts according to a first embodiment;

10 Figs. 2A and 2B are a perspective view and a sectional view showing the structure of a holding jig to be used in a manufacturing method for electronic parts according to a second embodiment;

Figs. 3A and 3B are a perspective view and a sectional view showing the structure of a holding jig to be used in a manufacturing method for electronic parts according to a third embodiment;

Fig. 4 is a perspective view showing a holding method for electronic parts;

Figs. 5A, 5B, and 5C are sectional views showing steps in each process in a manufacturing method for electronic parts;

Figs. 6A and 6B are sectional views showing steps in each process in another manufacturing method for electronic parts;

20 Fig. 7 is a perspective view showing the state of a holding jig used in a conventional manufacturing method for electronic parts;

Fig. 8 is a perspective view showing the structure of a holding jig to be used in a manufacturing method for electronic parts according to a fourth embodiment;

25 Figs. 9A, 9B, and 9C are sectional views showing steps in each process in
the manufacturing method for electronic parts shown in Fig. 8;

Fig. 10 shows the relationship between the rubber hardness of elastic materials and the bonding strength of wire bonding;

Fig. 11 shows the relationship between the thickness of elastic materials and the rubber hardness; and

5 Figs. 12A and 12B are sectional views showing steps in each process in a manufacturing method for electronic parts according to a fifth embodiment.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

A holding jig for an electronic part according to a first embodiment is described with reference to Figs. 1A and 1B.

10 Figs. 1A and 1B are a perspective view and a sectional view of a holding
jig for holding an electronic part or component thereof to be manufactured. In
Figs. 1A and 1B, a hard plate 1 made of metal and an elastic material 2 laminated
on the surface of the hard plate 1 are shown. It is desirable that the hardness of
the elastic material 2 be a rubber hardness degree of A30 or more according to a
15 type A durometer hardness test specified in Japanese Industrial Standard JIS K
6253, Hardness testing methods for vulcanized rubber. Moreover, the elastic
material 2 is influenced by the plate 1 as a foundation, depending on the thickness
of the elastic material 2, but the rubber hardness is set to substantially be a rubber
hardness degree of A30 or more. For example, high-temperature resistant rubber
20 containing silicone resin as a major constituent of the rubber is used.

In Figs. 1A and 1B, the elastic material 2 is made conductive overall by adding conductive particles such as carbon powder to the silicone resin as a major constituent and by dispersing the powder.

When a rubber material having a low elastic modulus is used for the elastic material 2, the rebound elasticity is lowered and at the same time a viscoelasticity is increased. This viscoelasticity makes a substrate 3 adhere to the surface of the elastic material. For example, soft silicone rubber shows an adhesive strength in

the range of 1 to 10 g/mm². As the adhesive strength is dependent on the particle size and dispersed density of the above-mentioned conductive powder, the manufacturing conditions of the elastic material are set so that the adhesive strength and the electric conductivity are in a fixed range, respectively.

5 Because the elastic material has been made conductive in this way, an electronic part and a component thereof being held on the surface of the elastic material are not subjected to electrostatic discharge damage, or adsorption or repulsion because of electrostatic charge, and the process and equipment for removing electrostatic charges becomes unnecessary.

10 Next, the structure of a holding jig according to a second embodiment is described with reference to Figs. 2A and 2B.

15 In Figs. 2A and 2B, a hard plate 1 made of metal and an elastic material 2 laminated on the surface of the hard plate 1 are shown. On the surface of this elastic material 2, a pattern using a conductive film 9 is formed and the end portion of the pattern is made conductive to the plate 1. This conductive film 9 is patterned so that while a plurality of electronic parts or components thereof are held on the surface of the elastic material 1, each of the electronic parts or components, which is in contact with part of the conductive film, becomes electrically conductive. In this example, a lattice pattern is made to have a narrower pitch in length and width than the width of the parts. Such a pattern is not limited to the lattice pattern and may be freely combined with, for example, a concentric pattern and a radiating pattern. However, a pattern, in which a plurality of conductive paths from the circumference (end surface) of the elastic material 2 to the outside such as the plate 1 are arranged to reduce the surface resistance, and in which even if part of the wiring is disconnected, electrical conduction is secured by another path, is desirable.

20 The above-mentioned conductive film is formed by a method of printing a resin material with conductive materials added thereto (conductive paste), a

method of making a film by plating, a method of making a film by dry processing such as vacuum evaporation and sputtering, a method of attaching a metal wire and metal foil, or the like.

5 Next, the structure of a holding jig according to a third embodiment is described with reference to Figs. 3A and 3B.

In Figs. 3A and 3B, a hard plate 1 made of metal and an elastic material 2 laminated on the surface of the hard plate 1 are shown. In this example, a wiring using a conductive material 10 is installed inside the elastic material 2, the upper portion of the conductive material 10 is exposed over the surface of the elastic material 2, and the lower portion is made conductive to the plate 1. The wiring using the conductive material 10 is arranged by a method of molding the conductive material 10 at the same time the elastic material 2 is molded, a method of embedding the conductive material 10 after the elastic material 2 has been molded, or the like.

15 Next, a method of holding electronic parts and a manufacturing method of electronic parts are described with reference to Figs. 4, 5A, 5B, 5C, 6A and 6B.

Fig. 4 is a perspective view showing the structure of a holding jig for holding substrates, and Figs. 5A to 5C show a manufacturing process for an electronic part in which an element is mounted on a substrate by die bonding and wire bonding.

20 In Fig. 4, a hard plate 1 of metal and an elastic material 2 laminated on the surface of the plate 1 are shown. The elastic material 2 is conductive, in a similar manner to the structure shown in the second embodiment. As described hereinafter, in a substrate mounting process, a plurality of substrates, each constituting a component of an electronic part, are arranged on the surface of the elastic material 2. The substrate 3 may include, for example, any of a resin substrate of a glass epoxy group, a ceramic substrate of alumina, and the like, or a laminate thereof can be used. Furthermore, any electronic part such as a passive

component, for example, may be loaded at a fixed location of the substrate 3 in advance.

As shown in Fig. 5A, in a substrate mounting process, a plurality of substrates 3 are arranged at fixed locations on the surface of an elastic material 2 by using a mounter. This mounter is a general-purpose machine which functions to chuck a work piece, such as a substrate, at a fixed location, move it, rotate it as required, and place it at a fixed location. In the drawing, a nozzle 16 for vacuum chucking is shown. That is, the mounter repeats an operation for removing a substrate one by one from a cartridge containing a plurality of substrates by vacuum chucking and for placing the substrate at a fixed location on the elastic material 2. In this way, as shown in Fig. 4, a plurality substrates 3 are arranged on the surface of the elastic material 2. As the surface of the elastic material 2 is adhesive, a plurality of substrates can be arranged and fixed only when the mounter places the substrates in order on the surface of the elastic material 2.

In a subsequent die bonding process, as shown in Fig. 5B, an element 4 such as a semiconductor chip is die bonded on the substrate 3. In the drawing, a nozzle 17 of the mounter for picking up the element 4 from a fixed location and for placing the element 4 on the substrate is shown. In this die bonding process, a heating process for hardening an adhesive, to glue the element 4 at a fixed location on the substrate 3 is included.

In the subsequent wire bonding process, as shown in Fig. 5C, a bonding pad exposed on the surface of the element 4 and an electrode formed on the substrate 3 are connected by a bonding wire 5. In the drawing, a capillary 18 of the wire bonder is shown.

The element 4 represents, for example, a semiconductor element, piezoelectric element, dielectric element, glass element, and may represent any element to be mounted on the substrate and be electrically connected.

Furthermore, various methods for wire bonding such as, for example, a method of ball bonding, a method of wedge bonding, and the like, can be applied. In these wire bonding processes, a process for applying ultrasonic waves to the bonding portion of the wire is included.

5 Figs. 6A and 6B show the sequence of a manufacturing method for an electronic part where an element is mounted on a substrate by bump bonding.

10 First, in a substrate mounting process, as shown in Fig. 6A, a plurality of substrates 3 are disposed at fixed locations on the surface of an elastic material 2 by using a mounter. In the drawing, a nozzle 4 for vacuum chucking of the mounter is shown.

15 Next, as shown in Fig. 6B, an element 4 provided with a bump 6 in advance is joined at a fixed location on the substrate 3 by a bump bond. That is, flip chip bonding is performed. In the drawing, a collet 19 for positioning the element and for applying ultrasonic waves is shown.

20 As a bump electrode, any of a gold (Au) bump, a solder bump, a resin bump, for example, can be used.

25 The above-mentioned manufacturing processing can also be applied to a process where bumps are fitted to semiconductor chips, a mounting process where caps are glued on electronic parts, and the like, except die bonding and wire bonding processes. Furthermore, the manufacturing processing can be applied to a characteristics measurement process for semi-finished and finished products, an adjustment process for trimming, and various processes such as a simple storing process, transferring process, and the like.

As described above, because a substrate 3 is made to be held by the adhesiveness of the surface of an elastic material 2, the positional variation and the movement between processes of the substrate 3 are prevented, and in particular by utilizing a mounter in the substrate mounting process, the alignment (stability of the pitch, rotational position, for example) of the substrate 3 is improved and

recognition errors are suppressed in automation of each process. Furthermore, because the elastic material 2 is conductive, the elastic material 2 and the substrate 3 are prevented from being charged because of electrostatic charges, and the discharge current caused by the static electricity is prevented from flowing through the element mounted on the substrate.

Furthermore, because a space for weighting the substrate 3 with a pressure leaf spring is not required, miniaturization of electronic parts is facilitated.

Furthermore, even if the size and arrangement pattern of the substrate 3 are different, because a common holding jig can be used, manufacturing costs can be reduced.

Moreover, in the embodiments, the hard plate 1 was disposed on the lower surface of the elastic material 2, but provision of this plate is not necessarily required.

A manufacturing method of electronic parts according to a fourth embodiment is described with reference to Figs. 8 to 11.

Fig. 8 is a perspective view showing the structure of a holding jig for holding substrates. In Fig. 8, a hard plate 1 made of metal and an elastic material 2 laminated on the upper surface of the plate 1 are shown. It is desirable that the hardness of the elastic material 2 be a rubber hardness degree of A30 or more according to a type A durometer hardness test specified in Japanese Industrial Standard JIS K 6253, Hardness testing methods for vulcanized rubber. Moreover, as described later, the elastic material 2 is influenced by the plate 1 as a foundation, depending on the thickness of the elastic material 2, but the rubber hardness is set to substantially be a rubber hardness degree of A30 or more.

Specifically, rubber containing silicone resin as its major constituent and having a heat-resistance temperature of 250°C is used.

In Fig. 8, substrates 3, each constituting a component of an electronic part, are shown as being disposed on the elastic material 2. When a rubber material 1

having a low elasticity, such as silicone rubber is used, the repulsive force is lowered, and at the same time, viscoelasticity is increased. Because of this viscoelasticity, the substrates 3 are made to adhere to the surface of the elastic material. For example, in the case of soft silicone rubber, an adhesive strength in 5 the range of 1 to 10 g/mm² is shown. The disposition of the substrates 3 on the elastic material 2 may be made by hand, but it is desirable to make the arrangement by using a mounter in order to shorten the processing time when the location and direction of the substrate are automatically derived through image processing, for example, and to reduce the number of manual processes.

10 The substrates 3 may include, for example, any of a resin substrate of a glass epoxy group, a ceramic substrate of alumina, or a laminate thereof can be used. Furthermore, any electronic part such as a passive component, for example, may be loaded at a fixed location of the substrate 3 in advance.

15 Thus, because an elastic material is laminated on the upper surface of a hard flat plate 1, the flatness of the surface of the elastic material is increased and the positional accuracy of a substrate to be disposed on the elastic material and the positional accuracy of the element to be mounted on the substrate are not degraded.

20 Figs. 9A, 9B, and 9C show the manufacturing procedure of an electronic part in which an element is mounted on a substrate by die bonding and wire bonding.

25 First, in a substrate mounting process, as shown in Fig. 9A, a plurality of substrates 3 are arranged at fixed locations on the surface of an elastic material 2 by using a mounter. This mounter is a general-purpose machine which functions to chuck a work piece such as a substrate at a fixed location, move it, rotate it as required, and place it at a fixed location. In the drawing, a nozzle 16 for vacuum chucking is shown. That is, the mounter repeats an operation for removing a

substrate one by one from a cartridge containing a plurality of substrates by vacuum chucking and for placing the substrate at a fixed location on the elastic material 2. In this way, as shown in Fig. 8, a plurality of substrates 3 are arranged on the surface of the elastic material 2. As the surface of the elastic material 2 is adhesive, a plurality of substrates can be arranged and fixed only when the mounter places the substrates in order on the surface of the elastic material 2.

Moreover, because the rubber hardness of the elastic material is a rubber hardness degree of A30 or more, the substrate is hardly displaced on the elastic material in the direction of the substrate of the elastic material or in the vertical direction to the surface, and high positional accuracy of the substrate is maintained.

In the following die bonding process, as shown in Fig. 9B, an element 4, such as a semiconductor chip, is die bonded on the substrate 3. In the drawing, a nozzle 17 of the mounter for picking up the element 4 from a fixed location and for placing the element 4 on the substrate is shown. In this die bonding process, a heating process for hardening an adhesive to glue the element 4 at a fixed location on the substrate 3 is included. For this purpose, a heat-resistant material having a heat-resistance temperature of 250°C is used so that the heat-resistance temperature of the elastic material 2 becomes higher than a heating temperature of 100 to 200°C in the die bonding process.

In a subsequent wire bonding process, as shown in Fig. 9C, a bonding pad exposed on the surface of the element 4 and an electrode formed on the substrate 3 are connected by a bonding wire 5. In the drawing, a capillary 18 of the wire bonder is shown. In this wire bonding process, the heating temperature is also in the range of 100 to 200°C and the heat-resistance temperature of the elastic material 2 is higher than this temperature.

The element 4 represents, for example, a semiconductor element, piezoelectric element, dielectric element, glass element, and represents any element if the element is attached on the substrate and is electrically connected.

Furthermore, various methods for wire bonding such as, for example, a 5 method of ball bonding, a method of wedge bonding, can be applied. In these wire bonding processes, a process for applying ultrasonic waves to the bonding portion of the wire is included.

Fig. 10 shows the relationship between rubber hardness and bonding 10 strength of wire. As measurement conditions, a common wire bonding method where ultrasonic waves are applied was used for wire bonding, and regarding a plurality of samples where the elastic materials have a plurality of levels of rubber hardness, their wire-bond pull strength (strength measured by a wire-bond pull test) was measured. Furthermore, when wire bonding was performed using a conventional tray made of metal, but without using the elastic material 2 and the 15 plate 1, the wire-bond pull strength was measured and is shown as a reference value.

Based on the result shown in Fig. 10, when the rubber hardness of the 20 elastic material is a rubber hardness degree of A28 or less, the bond strength of the wire deteriorates, but it is understood that when the rubber hardness is a rubber hardness degree of A30 or more, a strength equivalent to the reference value can be obtained. Therefore, it is desirable to make the rubber hardness of a rubber hardness degree of A30 or more.

Fig. 11 shows the relationship between the rubber thickness and the rubber 25 hardness of the above-mentioned elastic materials. The rubber hardness according to Japanese Industrial Standard JIS K 6253 is constant when the rubber thickness is 6 mm or more. When the rubber thickness is less than 6 mm, the rubber hardness is affected by the foundation, and as the rubber thickness decreases, the rubber

hardness tends to substantially increase. Here, the hardness of the elastic material itself is not important, but the rubber hardness becomes important. Therefore, in the examples shown in Fig. 11, when an elastic material having a rubber thickness of 6 mm or more is used, rubber A, B, and C are used. However, when an elastic material having a rubber thickness of less than 3 mm is used, rubber D may also be used. In the same way, when the rubber thickness is less than 1.8 mm, rubber E may also be used.

As described above, because a substrate 3 is made to be held by the adhesiveness of the surface of an elastic material 2, positional variation and movement between processes of the substrate 3 are prevented, and in particular, by utilizing a mounter in a substrate mounting process, the alignment (stability of the pitch, rotational position, for example) of the substrate 3 is improved and recognition errors are suppressed in automation in each process.

Furthermore, because the space for weighting a substrate 3 with a pressure leaf spring is not required and the wasteful space is removed, miniaturization of electronic parts is facilitated.

Furthermore, even if the size and arrangement pattern of substrates are different, because a common holding jig can be used, manufacturing costs can be reduced.

Furthermore, because the rubber hardness of an elastic material is made to be a rubber hardness degree of A30 or more, the bonding energy produced by ultrasonic waves in wire bonding is hardly absorbed by the elastic material, and accordingly a bonding strength equivalent to that in the case in which a conventional metal tray is used can be obtained. Furthermore, when the primary material of an elastic material 2 is silicone resin and a holding jig is heat-resistant, it may be fitted for the heating process in an existing die bonding process and wire bonding process.

Next, a manufacturing method for electronic parts according to a fifth embodiment is described with reference to Fig. 12. Fig. 12 shows the procedure of a manufacturing method for electronic parts in which an element is mounted on a substrate by a bump bond.

5 First, in a substrate mounting process, as shown in Fig. 12A, a plurality of substrates 3 are arranged at fixed locations on the surface of an elastic material by using a mounter. In the drawing, a nozzle 4 for vacuum chucking of the mounter is shown.

10 Next, as shown in Fig. 12B, an element 4 provided with a bump 6 in advance is joined at a fixed location on the substrate 3 by a bump bond. That is, flip chip bonding is performed. In the drawing, a collet 19 for positioning the element 4 and for applying ultrasonic waves is shown. In this bump bond process, the substrate 3 is heated to 100 to 200°C. At this time, because the heat-resistance temperature of the elastic material 2 is 250°C, the elastic material does not 15 deteriorate.

As a bump electrode, any of a gold (Au) bump, a solder bump and a resin bump, for example, can be used.

20 Regarding the bonding energy produced by the ultrasonic waves, the absorption of the bonding energy is suppressed by making the rubber hardness of the elastic material 2 a rubber hardness degree of 30 or more, and in the same way as in the wire bonding shown in Fig. 11, a bonding strength equal to that in the case where a conventional metal tray is used can be obtained.

Moreover, in the embodiments, a hard plate 1 is disposed on the lower surface of the elastic material 2, but this plate is not necessarily required.

25 Although this invention has been illustrated and described in accordance with certain preferred embodiments, it is recognized that the scope of this

invention is to be determined by the following claims. All of the aforementioned documents are each incorporated by reference herein in each of their entireties.

WHAT IS CLAIMED IS:

1. A holding jig comprising:
 2. an elastic material wherein at least the surface thereof is adhesive and conductive, and wherein an electronic part or component constituting the electronic part is holdable by the adhesive strength of the surface of the elastic material.
 1. 2. The holding jig according to claim 1, wherein the elastic material is made to be conductive by adding conductive material to the elastic material.
 1. 2. 3. The holding jig according to claim 1, wherein the elastic material is made to be conductive by installing a wiring using conductive material on the surface of the elastic material.
 1. 2. 3. 4. The holding jig according to claim 1, wherein the elastic material is made to be conductive by installing a wiring using conductive material inside the elastic material, the wiring being exposed on the surface of the elastic material.
 1. 2. 5. A method of holding an electronic part or a component constituting the electronic part, comprising:
 3. holding said electronic part or a component constituting the electronic part on a surface of an elastic material, in which at least the surface of a said elastic material is adhesive and conductive, by the adhesive strength of said surface.

1 6. A method of manufacturing electronic parts, comprising:
2 holding a substrate on a surface of an elastic material, in which at least the
3 surface of said elastic material is adhesive and conductive, by the adhesive strength
4 of said surface; and

5 mounting and electrically connecting an element on said substrate while said
6 substrate is held on the surface of said elastic material.

1 7. A method of manufacturing electronic parts, comprising:
2 holding a substrate on a surface of an elastic material, in which at least the
3 surface of said elastic material is adhesive, by the adhesive strength of said
4 surface; and

5 mounting and electrically connecting an element on said substrate while the
6 substrate is held on the surface of the elastic material.

1 8. The method of manufacturing electronic parts according to claim 7,
2 further including, applying ultrasonic waves to the bonding portion at which the
3 electric connection is performed.

1 9. The method of manufacturing electronic parts according to claim 7,
2 wherein the hardness of the elastic material is a rubber hardness degree of at least
3 A30.

1 10. The method of manufacturing electronic parts according to claim 7,
2 wherein the holding jig comprises heat-resistant material having a heat-resistance
3 temperature of about 250°C.

1 11. The method of manufacturing electronic parts according to claim 7,
2 wherein the holding jig includes a laminate structure of a hard plate and the elastic
3 material.

1 12. The method of manufacturing electronic parts according to claim 7,
2 wherein the elastic material comprises silicone resin.

1 13. The method of manufacturing electronic parts according to claim 7,
2 wherein the mounting process includes a wire bonding process.

1 14. The method of manufacturing electronic parts according to claim 7,
2 wherein the mounting process includes a bump bonding process.

ABSTRACT OF THE DISCLOSURE

An elastic material, in which at least the surface thereof is adhesive and conductive, is provided on a plate, and while a substrate constituting a component of an electronic part is held on the surface of the elastic material by the adhesiveness of the elastic material, an element such as a semiconductor chip is mounted at a fixed location on the substrate.

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特願平11-291576 頁: 1/ 4

【書類名】 図面

【図1】

FIG. 1 A (A)

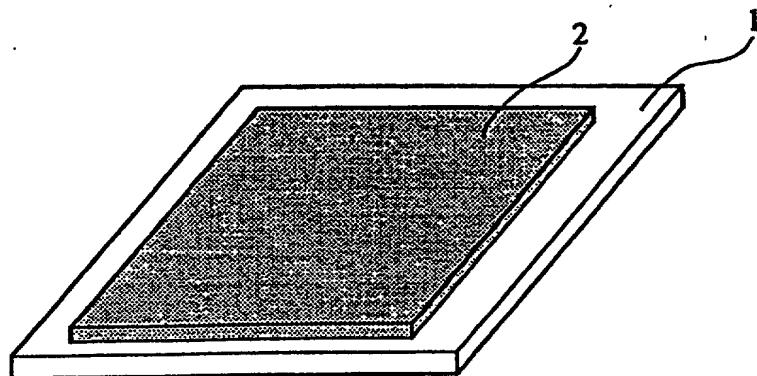


FIG. 1 B (B)



【図2】

FIG. 2 A

(A)

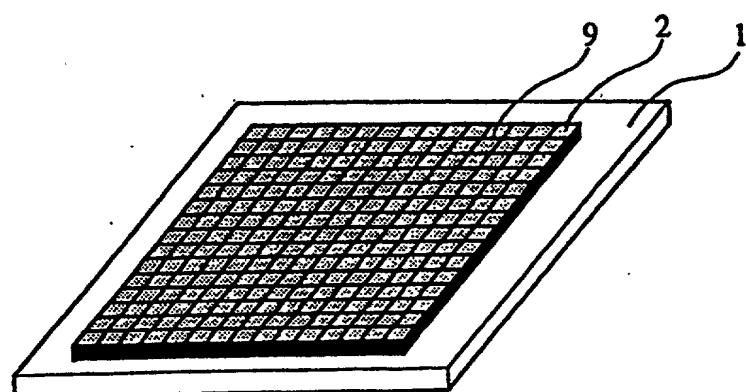
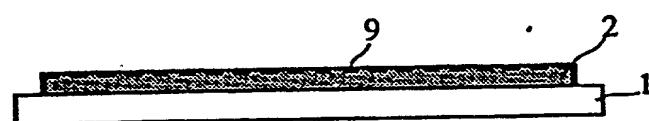


FIG. 2 B

(B)



【図3】

FIG. 3 A

(A)

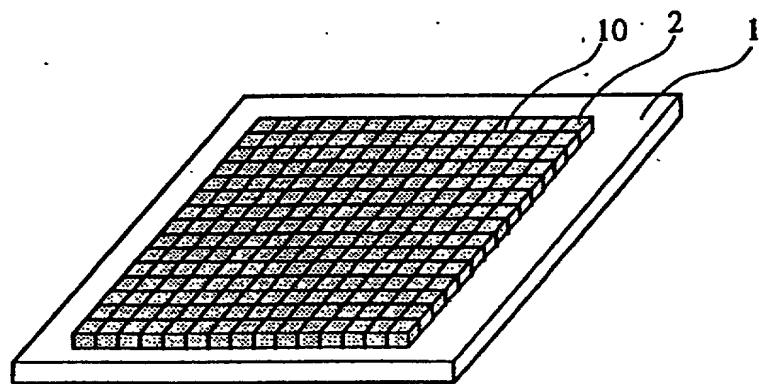
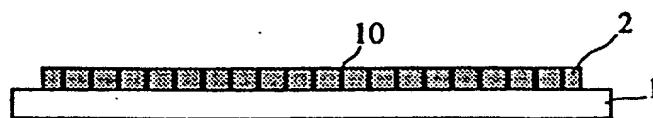


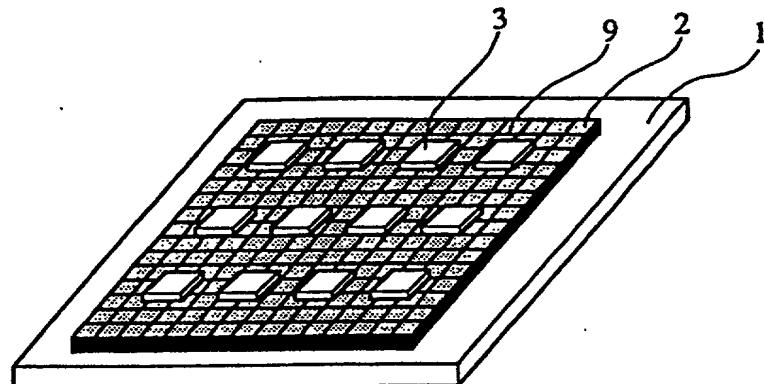
FIG. 3 B

(B)



【図4】

FIG. 4

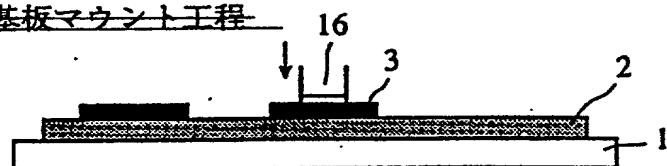


【図5】

SUBSTRATE MOUNTING PROCESS

FIG. 5 A

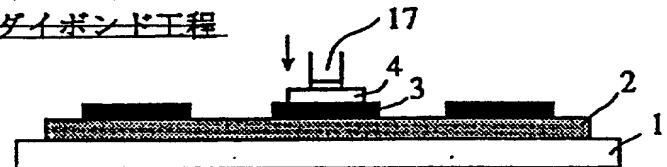
(a) 基板マウント工程



DIE BONDING PROCESS

(b) ゲイボンド工程

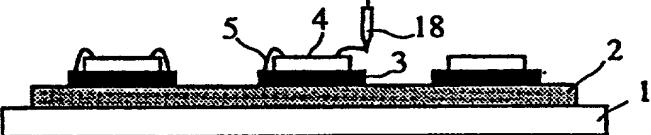
FIG. 5 B



WIRE BONDING PROCESS

(c) ワイヤボンド工程

FIG. 5 C

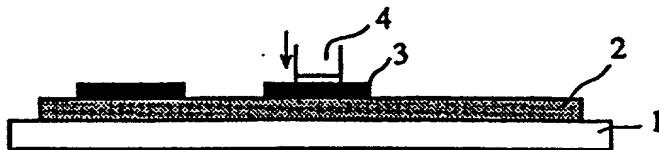


【図 6】

SUBSTRATE MOUNTING PROCESS

(a) 基板マウント工程

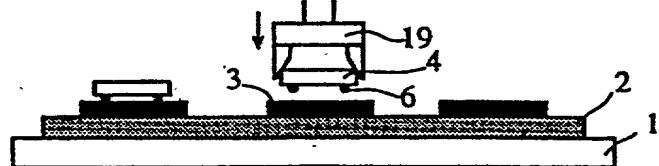
FIG. 6A



BUMP BONDING PROCESS

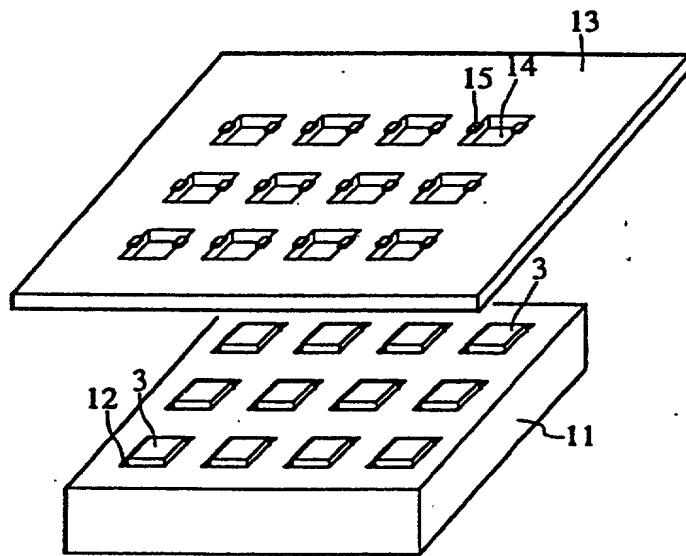
(b) バンプ接合工程

FIG. 6B



【図 7】

FIG. 7



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【書類名】図面

【図8】
8

FIG. 8

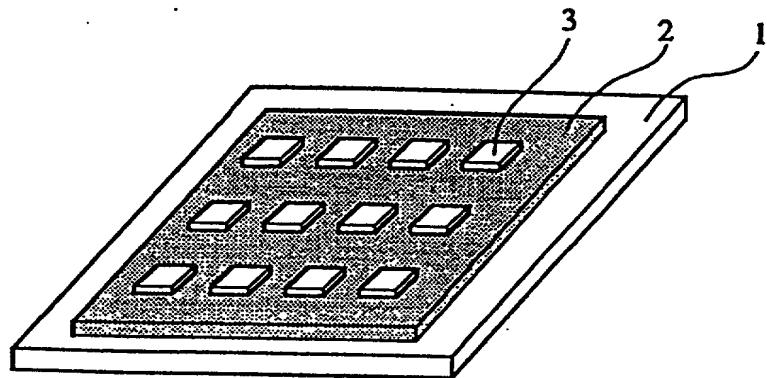
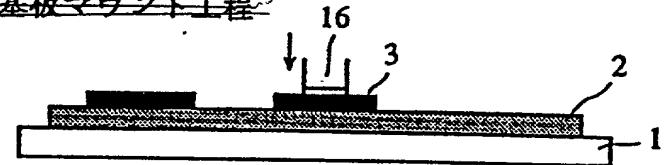
【図9】
9

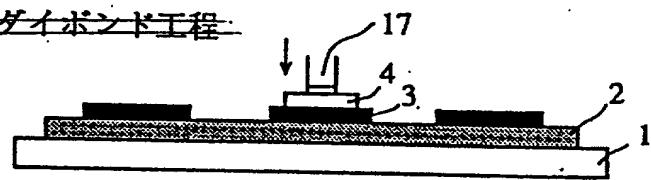
FIG. 9 A

SUBSTRATE MOUNTING PROCESS
(a) 基板マウント工程

DIE BONDING PROCESS

(b) ダイボンド工程

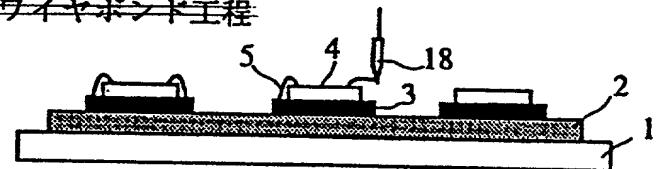
FIG. 9 B



WIRE BONDING PROCESS

(c) ワイヤーボンド工程

FIG. 9 C



【図8】
10

FIG. 10

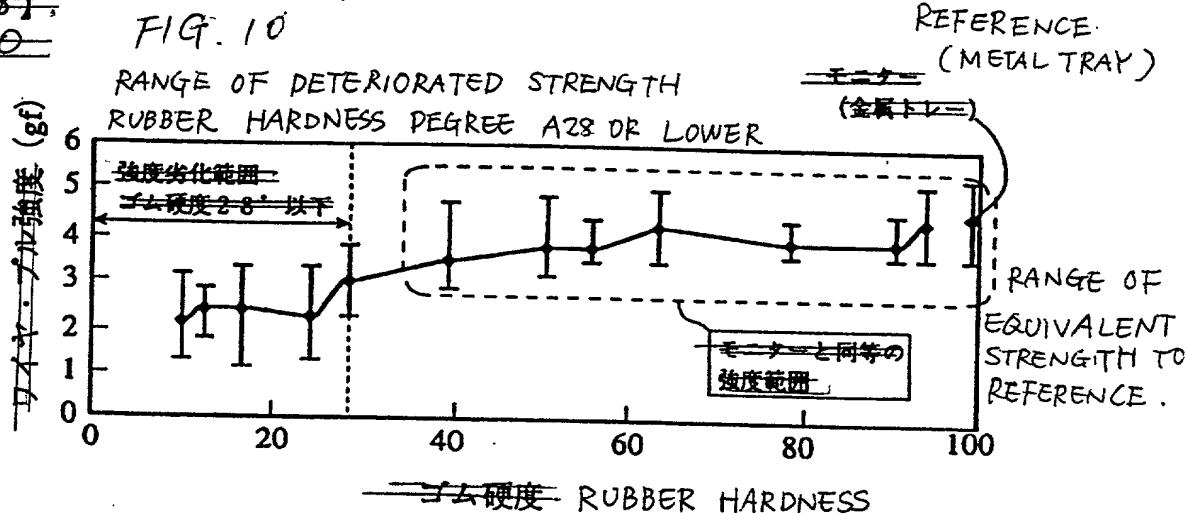
WIRE-BOND
PULL
STRENGTH【図9】
11

FIG. 11

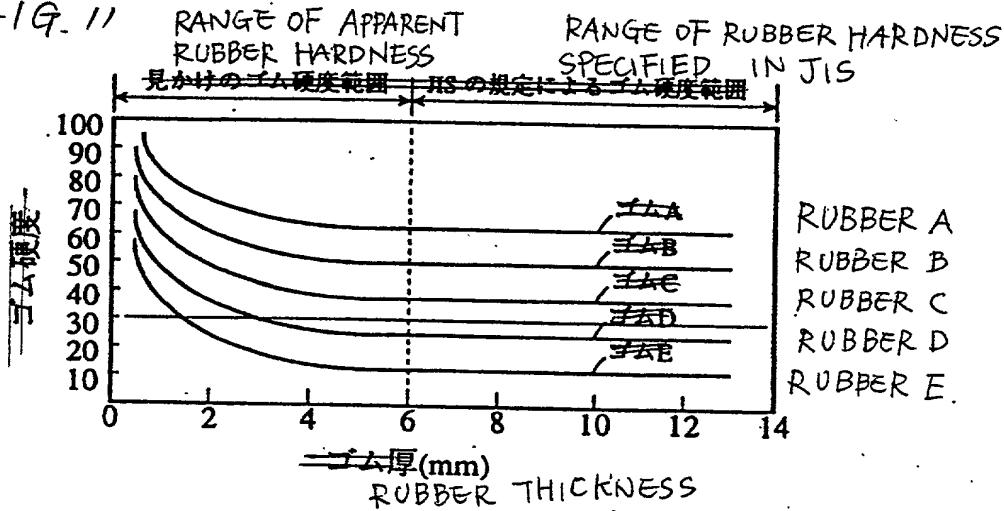
RUBBER
HARDNESS【図10】
12

FIG. 12 A

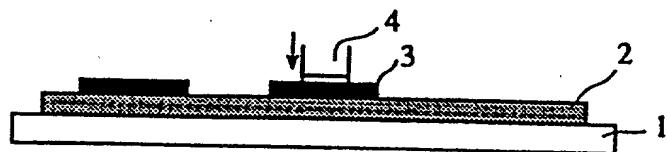
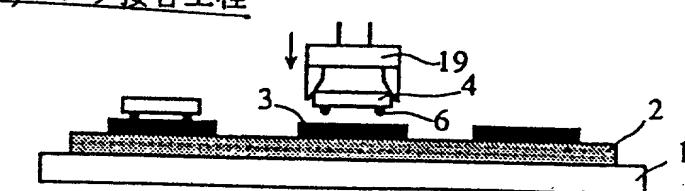


FIG. 12 B



**COMBINED DECLARATION AND POWER OF ATTORNEY
FOR UTILITY PATENT APPLICATION**

Attorney's Docket No.

018976-181

As a below-named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name;

I BELIEVE I AM THE ORIGINAL, FIRST AND SOLE INVENTOR (if only one name is listed below) OR AN ORIGINAL, FIRST AND JOINT INVENTOR (if more than one name is listed below) OF THE SUBJECT MATTER WHICH IS CLAIMED AND FOR WHICH A PATENT IS SOUGHT ON THE INVENTION ENTITLED:

HOLDING JIG FOR ELECTRONIC PARTS, HOLDING METHOD THEREFOR, AND MANUFACTURING

METHOD FOR ELECTRONIC PARTS

the specification of which

(check one) is attached hereto;

was filed on _____ as

Application No. _____

and was amended on _____;
(if applicable)

I HAVE REVIEWED AND UNDERSTAND THE CONTENTS OF THE ABOVE-IDENTIFIED SPECIFICATION, INCLUDING THE CLAIMS, AS AMENDED BY ANY AMENDMENT REFERRED TO ABOVE;

I ACKNOWLEDGE THE DUTY TO DISCLOSE TO THE OFFICE ALL INFORMATION KNOWN TO ME TO BE MATERIAL TO PATENTABILITY AS DEFINED IN TITLE 37, CODE OF FEDERAL REGULATIONS, Sec. 1.56 (as amended effective March 16, 1992);

I do not know and do not believe the said invention was ever known or used in the United States of America before my or our invention thereof, or patented or described in any printed publication in any country before my or our invention thereof or more than one year prior to said application; that said invention was not in public use or on sale in the United States of America more than one year prior to said application; that said invention has not been patented or made the subject of an inventor's certificate issued before the date of said application in any country foreign to the United States of America on any application filed by me or my legal representatives or assigns more than twelve months prior to said application;

I hereby claim foreign priority benefits under Title 35, United States Code Sec. 119 and/or Sec. 365 of any foreign application(s) for patent or inventor's certificate as indicated below and have also identified below any foreign application for patent or inventor's certificate on this invention having a filing date before that of the application(s) on which priority is claimed:

COMBINED DECLARATION AND POWER OF ATTORNEY		Attorney's Docket No. 018976-181
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COUNTRY/INTERNATIONAL	APPLICATION NUMBER	DATE OF FILING (day, month, year)	PRIORITY CLAIMED
JAPAN	11-291575	13 October 1999	YES <input checked="" type="checkbox"/> NO <input type="checkbox"/>
JAPAN	11-291576	13 October 1999	YES <input checked="" type="checkbox"/> NO <input type="checkbox"/>

I hereby appoint the following attorneys and agent(s) to prosecute said application and to transact all business in the Patent and Trademark Office connected therewith and to file, prosecute and to transact all business in connection with international applications directed to said invention:

William L. Mathis	17,337	R. Danny Huntington	27,903	Gerald F. Swiss	30,113
Robert S. Swecker	19,885	Eric H. Weisblatt	30,505	Charles F. Wieland III	33,096
Platon N. Mandros	22,124	James W. Peterson	26,057	Bruce T. Wieder	33,815
Benton S. Duffett, Jr.	22,030	Teresa Stanek Rea	30,427	Todd R. Walters	34,040
Norman H. Stepno	22,716	Robert E. Krebs	25,885	Ronni S. Jillions	31,979
Ronald L. Grudziecki	24,970	William C. Rowland	30,888	Harold R. Brown III	36,341
Frederick G. Michaud, Jr.	26,003	T. Gene Dillahunt	25,423	Allen R. Baum	36,086
Alan E. Kopecki	25,813	Patrick C. Keane	32,858	Steven M. du Bois	35,023
Regis E. Slutter	26,999	Bruce J. Boggs, Jr.	32,344	Brian P. O'Shaughnessy	32,747
Samuel C. Miller, III	27,360	William H. Benz	25,952	Kenneth B. Leffler	36,075
Robert G. Mukai	28,531	Peter K. Skiff	31,917	Fred W. Hathaway	32,236
George A. Hovanec, Jr.	28,223	Richard J. McGrath	29,195		
James A. LaBarre	28,632	Matthew L. Schneider	32,814		
E. Joseph Gess	28,510	Michael G. Savage	32,596		



21839

and: Adam J. Cermak, Reg. No. 40,391; and Timothy B. Kang, Reg. No. 46,423

Address all correspondence to:



Platon N. Mandros
BURNS, DOANE, SWECKER & MATHIS, L.L.P.
P.O. Box 1404
Alexandria, Virginia 22313-1404

21839

Address all telephone calls to: Platon N. Mandros at (703) 836-6620.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

FULL NAME OF SOLE OR FIRST INVENTOR	SIGNATURE	DATE
Akio KATSUBE		
RESIDENCE	CITIZENSHIP	
Kanazawa-shi, Ishikawa-ken, JAPAN	JAPAN	
POST OFFICE ADDRESS		
c/o (A170) Intellectual Property Department, Murata Manufacturing Co., Ltd., 26-10, Tenjin 2-chome, Nagaokakyo-shi, Kyoto-fu 617-8555		
FULL NAME OF SECOND JOINT INVENTOR, IF ANY	SIGNATURE	DATE
Hideo NAKAGOSHI		
RESIDENCE	CITIZENSHIP	
Omihachiman-shi, Shiga-ken, JAPAN	JAPAN	
POST OFFICE ADDRESS		
c/o (A170) Intellectual Property Department, Murata Manufacturing Co., Ltd., 26-10, Tenjin 2-chome, Nagaokakyo-shi, Kyoto-fu 617-8555		

COMBINED DECLARATION AND POWER OF ATTORNEY

Attorney's Docket No.

018976-181

FULL NAME OF THIRD JOINT INVENTOR, IF ANY Hidemasa KAWAI	SIGNATURE	DATE
RESIDENCE Yoshida-gun, Fukui-ken, JAPAN	CITIZENSHIP JAPAN	
POST OFFICE ADDRESS c/o (A170) Intellectual Property Department, Murata Manufacturing Co., Ltd., 26-10, Tenjin 2-chome, Nagaokakyo-shi, Kyoto-fu 617-8555		

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